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Analog Circuit Design for Communication SOC

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About the ebook

This e-book provides several state-of-the-art analog circuit design techniques. It presents both empirical and theoretical materials for system-on-a-chip (SOC) circuit design. This is an excellent reference book for both circuit designers and researchers who are interested in the field of design of analog communication circuits for SOC applications.

Contents

- ▶ Introduction
- ▶ Introduction to SOC: Analog Communication Circuit Design Perspective
- ▶ Wireless Carrier Frequency Conversion: Mixer Design
- ▶ Wireless Signal Transmission: Power Amplifier Design
- ▶ System Timing Generation: Phase-Locked Loop Design
- ▶ Delay-Locked Loop and Clock Data Recovery for Wired Communications

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