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Switching Mode Circuit Analysis and Design Innovative Methodology by Novel Solitary Electromagnetic Wave Theory

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About the ebook

I joined NEC in 1968 and was assigned to the circuit development department of the computer division. My first work was the circuit design of the switching mode power supply (SMPS) for the mainframe of the computer system. My job area was expanded to development of the design rule and to standardization of all over the electric and electronics parts for the non-logic circuit. Non-logic circuit was categorized to except the gate-array which forms the logic circuit of the mainframe.

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- ▶ Overview of Circuit Design Technologies about SMC
- ▶ Electromagnetic Analysis of PCB
- ▶ Analysis of Performance of Decoupling Capacitor
- ▶ Validation of Decoupling Performance of Capacitor by PCB [1-3]
- ▶ Conformation of EMI Level and Suppressing Method of LSI
- ▶ Feasibility of Reconfiguring SMC to QSCC

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